
8 Bit Serial To Parallel Converter Vhdl Code For Digital Clock

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An 8-bit parallel/serial conversion system based on VHDL was proposed in this document. It consists of a 32-bit counter used to adjust the clock frequency. I don't know how it was included in the processor. I also don't know if I actually used this system. It was just in my mind. I used VHDL as a formal model to see how it works and how it can be used. Now I want to use this for other things. I'm not sure if this actually worked, but I think it would be easy to do something similar. Seems like I'd have to include all of this in my new processor, right?

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